十速科技股份有限公司
tenx technology inc.

PRODUCT NAME
TP66P0x series /TMU310x series

TITLE
Clear Interrupt flag

APPLICATION NOTE

■   Problem description:

When use BCF to clear an interrupt flag of F-plane address (0x11H,0x12H), we found some situation maybe clear other bit(s) which we don't want be cleared, so that, don't suggest that customer use BCF on F-plane 0x11H/0x12H.

■   Coding Suggestion:

Clear interrupt flag of F-plane 0x11H/0x12H use byte move instruction (MOVWF, CLRF) instead of bit clear instruction (BCF).

 For Example: (Don't be recommended)

```
    BCF       12H, 1                ; (clear 12H.1 interrupt flag)
```

For Example: (Recommended)

```
    MOVLW        0FDH        ; 8'b11111101 only bit1 be cleared, keep other bits.
    MOVWF        12H
```